

Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore (MP)

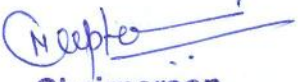
Choice Based Credit System (CBCS) Scheme

Bachelor of Technology (Electronics & Communication)

I - SEMESTER

Sr. No.	Subject Code	Name of Subject	Course Code	Teaching Scheme/ Week			Examination Scheme					Total Marks	Credits
				L	T	P	Theory			Practical			
							End_Sem University Exam	Two Term Exam	Teachers Asses-ment*	End Sem University Exam	Teachers Asses-ment*		
1	BTPH101	Applied Physics		3	1	2	60	20	20	30	20	150	5
2	BTMA101	Applied Mathematics-I		3	1	0	60	20	20	0	0	100	4
3	BTEE102	Fundamental of Electrical Engg.		3	1	2	60	20	20	30	20	150	5
4	BTEC102	Fundamental of Electronics Engg.		3	1	2	60	20	20	30	20	150	5
5	BTME101	Engineering Drawing		3	0	4	60	20	20	30	20	150	5
6	BTHU101	Communication Skills		0	0	2	0	0	0	0	50	50	1
7	BTCS101	Computer Programming-I		0	0	2	0	0	0	30	20	50	1
TOTAL				15	4	14	300	100	100	150	150	800	26

*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.


Chairperson
Board of Studies
Shri Vaishnav Vidyapeeth Vishwavidyalaya
Indore


Joint Registrar
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore